邏輯設計實習 0101-54 基本邏輯閘練習、**IO**板

姓名： 座號：

日期：\_\_\_\_\_年\_\_\_\_\_月\_\_\_\_\_\_\_日



一、實驗名稱：以基本邏輯閘實現布林式

二、實現F=AB+



| 三、真值表   | A | B | C | F | | --- | --- | --- | --- | |  |  |  |  | |  |  |  |  | |  |  |  |  | |  |  |  |  | |  |  |  |  | |  |  |  |  | |  |  |  |  | |  |  |  |  | | 四、電路圖   |  | | --- | |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |

四、使用TTL IC

| IC編號 | 邏輯閘 | 數量 | IC編號 | 邏輯閘 | 數量 | IC編號 | 邏輯閘 | 數量 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
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五、麵包板與IO板完成圖（實物照片）

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